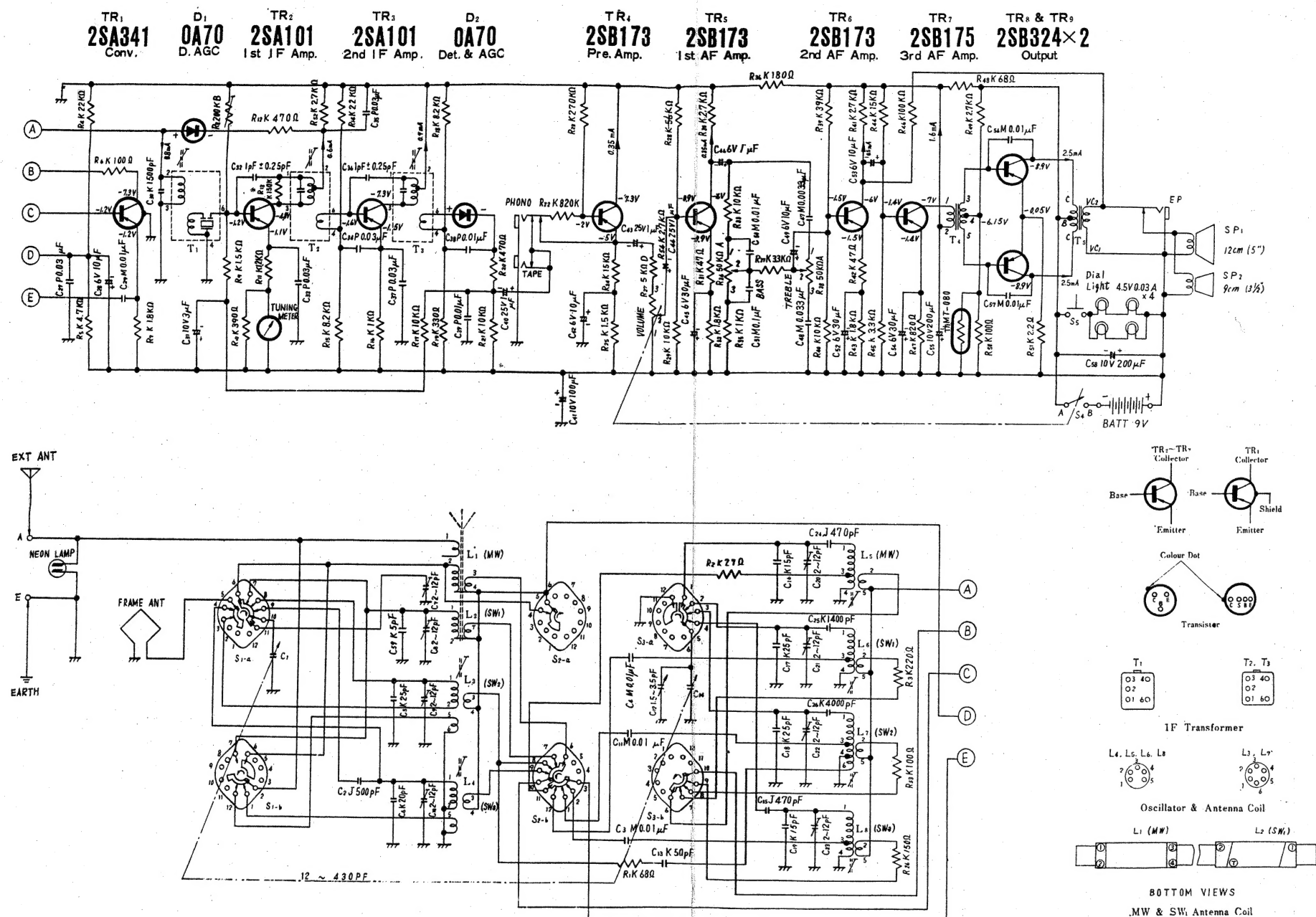




SCHEMATIC DIAGRAM MODEL T-100D



Notes:

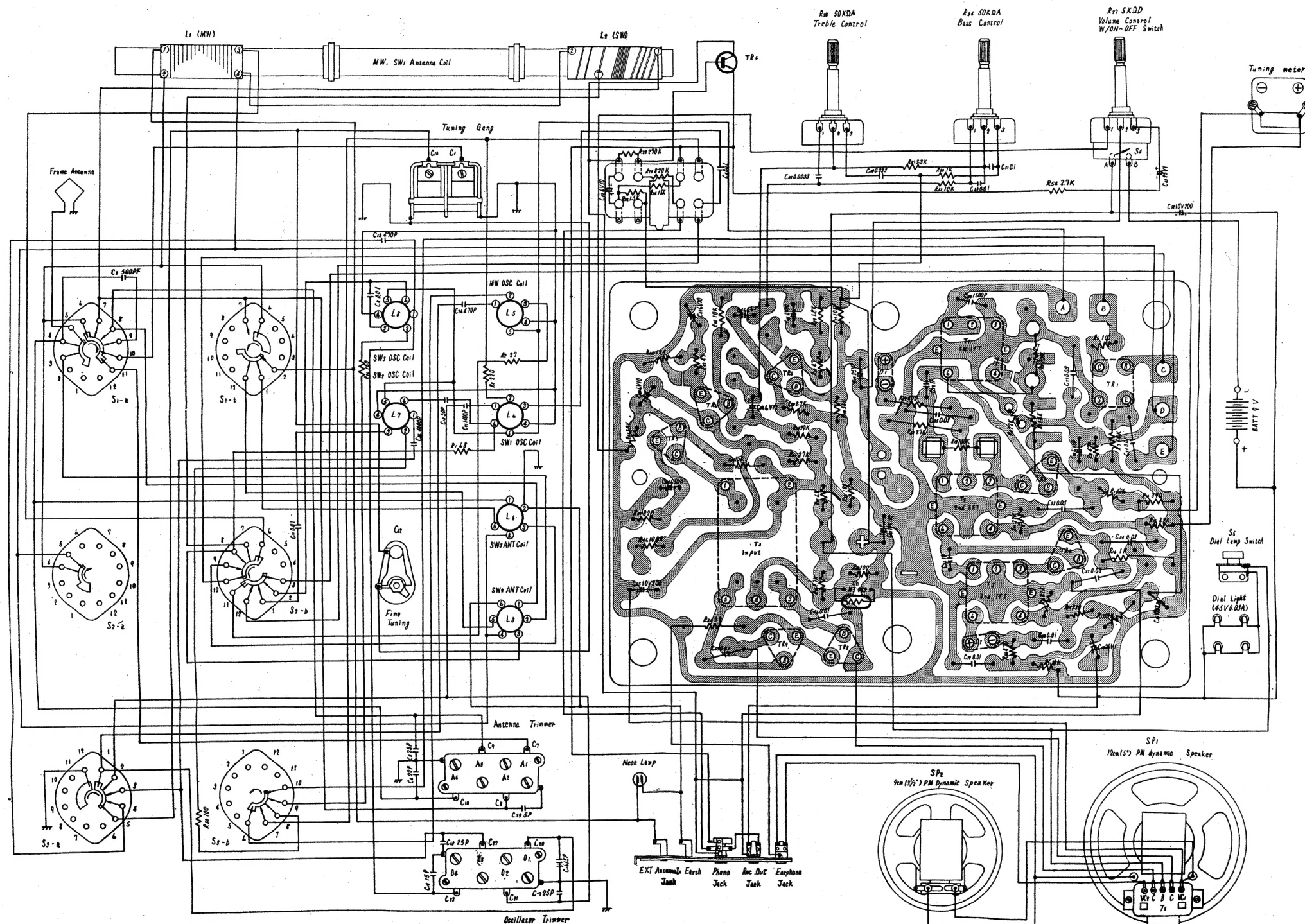
1. S₁~S₃: Band selector switch in "MW" position.
2. S₄: Power source switch in "OFF" position.
3. S₅: Dial lamp switch in "OFF" position.
4. Tone controls: R₃₄ BASS
R₃₈ TREBLE
5. Measured voltages for TR₁~TR₉ are from transistor terminal to bias line. [A]
6. DC Voltage measurements are taken with circuit tester (10KΩ/V).

7. Capital letters (M, K, J, P) in the circuit diagram show allowable tolerance of resistors and capacitors as follows.
M=±20% K=±10% J=±5% P=+100%
— 0%
8. Battery Current: No signal 23mA
Maximum output 300mA
9. PF=pico farad=mmf
μF=micro farad=MF

CIRCUIT BOARD

MODEL T-100D

Conductor View



Notes:

1. All resistor values in ohms (K=1000 Ω).
2. All capacitor values in micro farads (P= μ F).

3. S1~S3: Band selector switch in "MW" position.
4. S4: Power source switch in "OFF" position.
5. S10: Dial lamp switch in "OFF" position.

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